



```

chip ic4 PAL16L8
/global_enable=1 D6=2 D7=3 A8=4 A11=5 A15=6 pin7=7 pin8=8 A4=9
GND=10 A5=11 /ENABLE_CPC_SRAMADR=12 /WRITE_TO_SRAMDRBUFFER=13
WORD0_WORD1=14 /ENABLE_74LS670_SRAMADR=15 SRAM_A4=16 SRAM_A5=17
pin18=18 /ENABLE_SRAM=19 VCC=20

equations
ENABLE_SRAM = global_enable
+ A15 * /pin8
+ A8 * /A11 * A15 * /pin7
+ A8 * A11 * /A15 * /pin7
ENABLE_SRAM.oe = vcc

/pin18 = /A8 * /A4 * A5
pin18.oe = vcc

/SRAM_A5 = /A11 * /A5
+ A15 * /A5
+ /A8 * /A5
+ pin7 * /A5
+ /D7 * A8 * A11 * /A15 * /pin7
SRAM_A5.oe = /WORD0_WORD1

/SRAM_A4 = /A11 * /A4
+ A15 * /A4
+ /A8 * /A4
+ pin7 * /A4
+ /D6 * A8 * A11 * /A15 * /pin7
SRAM_A4.oe = /WORD0_WORD1

ENABLE_74LS670_SRAMADR = A8 * A15 * /pin8
+ D6 * /D7 * A8 * A11 * /A15 * /pin7
ENABLE_74LS670_SRAMADR.oe = vcc

/WORD0_WORD1 = A11 * pin8
+ /A8 * pin8
+ /A15
+ pin7 * pin8
WORD0_WORD1.oe = vcc

WRITE_TO_SRAMDRBUFFER = /A8 * A15 * /pin8
+ /D6 * /D7 * A8 * A11 * /A15 * /pin7
WRITE_TO_SRAMDRBUFFER.oe = vcc

ENABLE_CPC_SRAMADR = /A8 * /A15
+ /A11 * /A15
+ /A15 * pin7
+ /A8 * pin8
+ A11 * A15 * pin8
+ pin7 * pin8
ENABLE_CPC_SRAMADR.oe = vcc

chip ic5 PAL16L8
// -> XN00000011001XX +/M1 -> 0x6?
/IC3_P2 = (/IC4A . /A13 . /A12 . /A11 . /A10 . /A9 . /A7 . A6 .
/A3 . A2) . /M1

// -> XX111110 111010XX + Write IO
/IC3_P9 = ( A13 . A12 . A11 . A10 . A9 . A7 . A6 . A3 . /A2 ) .
/IC4A . ( /WR . /IORQ )

// -> X11X11X XXXXXXXX + Write io
/IC4_P7 = ( A14 . A13 . A12 . A10 . A9 ) . ( /WR . /IORQ )

// -> X011110X XXXXXXXX + Write io
/IC4_P8 = (/A14 . A13 . A12 . A11 . A10 . /A9 ) . ( /WR . /IORQ )

```

Giants (CPC-Hardware) - Jeff (HxC2001)

Title		
MultiFace 2		
Size	Document Number	Rev
C	http://hxc2001.free.fr	1
Date:	Tuesday, November 04, 2008	Sheet 1 of 1